
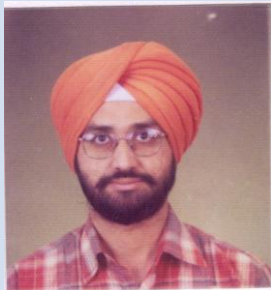



1.	<b>Name:</b>	<b>NIRMAL SINGH GREWAL</b> 
2.	<b>Designation:</b>	Associate Professor and Head
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	Feb 1983
5.	<b>Qualifications:</b>	1.M.Tech (Systems and Control Engineering)[1983] 2.BSc (Electrical Engineering)[1978]
6.	<b>Experience</b>	1.Asstt. Prof.(G.N.D.E.C.,Ludhiana)[Jan,2000 to till date] 2.Education Officer(Lae Technical College,Papua New Guinea)[Jul 1997 to Dec 1999] 3.Asstt. Prof.(G.N.D.E.C.,Ludhiana)[from Apr 1993 to Jul 1997] 4.Senior Lecturer(G.N.D.E.C.,Ludhiana)[from Feb 1989 to Apr 1993] 5.Lecturer(G.N.D.E.C.,Ludhiana)[from Feb 1983 to Feb 1989] 6. Design Engineer (Powerwell capacitor manufacturing company)[July 1978 to Nov 1980]
7.	<b>Paper published</b>	National Journals: 02 International Journals: 04
8.	<b>Paper presented in conferences</b>	Conferences: 40
9.	<b>PhD guide- field and university</b>	
10.	<b>PhDs/ M.Tech</b>	Master's: 20 Thesis
	<b>thesis guided</b>	PhD: Nil


<b>11.</b>	<b>Books published/IPRs./ Patents</b>	Nil
<b>12.</b>	<b>Professional Memberships</b>	Life member of ISTE. Member of institutional management committee of advance training institute,
<b>13.</b>	<b>Area of Consultancy</b>	
<b>14.</b>	<b>Awards</b>	Gold medalist in M-Tech
<b>15.</b>	<b>Grants Fetched</b>	Development of lab under MADROB from Govt. of India of Rs.10 lacs
<b>16.</b>	<b>Interaction with professional institutions</b>	<ul style="list-style-type: none"> <li>• Member of Board of Studies of PTU (Electronics and instrumentation)</li> <li>• Member of institutional management committee of advance training institute, Ludhiana</li> <li>• Executive Director Science and technology Enterprises park , GNDEC Ludhiana</li> <li>• Member of advisory board Department of industries and commerce , Punjab</li> </ul>

1.	<b>Name</b>	<b>DR. SANDEEP SINGH GILL</b>  
2.	<b>Designation:</b>	Professor and Dean ( Administrative)
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	1 May 1998
5.	<b>Qualifications:</b>	1. PhD (Electronics and communication) [2012] 2. M.Tech(Electronics & Comm. Engineering) [2005] 3. MBA( Marketing /Systems & Control) [1994] 4. B.E( Electronics & Comm. Engineering)[1991]
6.	<b>Experience</b>	1.Professor (18 February, 2013 onwards) 2. Associate/ Assistant Prof. (G.N.D.E.C., Ludhiana) [16August, 2005 till 18 <sup>th</sup> February, 2013] 3.Lecturer (G.N.D.E.C., Ludhiana)[1 May 1998 till 15 August 2005] 4. Centre Manager (Tulec Computer Education, Khanna) [March 1996-April 1998] 5.Senior Executive (Wipro Ltd., Jalna Road Aurangabad)[Jan 1994 to Dec. 1995]
7.	<b>Paper published</b>	National journals : 01 International Journals : 18
8.	<b>Paper presented in conferences</b>	Conferences:65

<b>9.</b>	<b>PhDs/ M.Tech thesis guided</b>	Master's: 50 PhD: 02 (Guiding)
<b>10.</b>	<b>Books published/IPR s./Patents</b>	1. "Handbook of Management", Pragati Prakashan, 2005. 2. Contributed essay on " Marketing in 21 <sup>st</sup> century", in book " Business Management- Parameters & Perspectives", edited by Dr. Ajay Prasher & Rajeev Prasher, Medallion Press, 2006.
<b>11.</b>	<b>PhD guide field and university</b>	Dr. Rajeevan Chandel Dr. Ashwani Kumar Chandel National institute of technology, Hamirpur
<b>12.</b>	<b>Professional Memberships</b>	ISTE, Ludhiana management association
<b>13.</b>	<b>Area of Consultancy</b>	VLSI design, Evolutionary Soft computing technologies
<b>14.</b>	<b>Awards</b>	
<b>15.</b>	<b>Grants Fetched</b>	1. Opto electronics and fiber optics projects (TAPTEC) 2. Upgradation of EDA Lab ( MODROBS) 3. DST, PTU, CSIR Grants for conduct for conference.
<b>16.</b>	<b>Interaction with professional institutions</b>	1. As expert in selection committees, conferences, STTPS, and in collaborated research. 2. Member board of studies, P.T.U 3. Reviewer in reputed journals and conferences. 4. Examiner at Ph.D / PG Level


1.	<b>Name:</b>	<b>KANWALJIT SINGH</b>
		
2.	<b>Designation:</b>	Associate Professor
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	July 87
5.	<b>Qualifications:</b>	1. M.E. (Electronics and Comm.engg)[1992] 2.B.E. (Electronics and Comm.engg)[1985]
6.	<b>Experience</b>	1. Assistant/Associate Prof. (G.N.D.Engg.College Ludhiana) [Dec.94-till date] 2.Lecturer(G.N.D.Engg.College Ludhiana)[July 87-Dec.94] 3.Service Engineer (H.C.L. Delhi)[July 1985-July 1987]
7.	<b>Paper published</b>	National Journals: Nil International Journals: 03
8.	<b>Paper presented in conferences</b>	Conferences: 30
9.	<b>PhD guide- field and university</b>	N.A
10.	<b>PhDs/ M.Tech thesis guided</b>	Master's: 25 PhD: Nil
11.	<b>Books</b>	Nil

	<b>published/IPRs./ Patents</b>	
<b>12.</b>	<b>Professional Memberships</b>	
<b>13.</b>	<b>Area of Consultancy</b>	
<b>14.</b>	<b>Awards</b>	
<b>15.</b>	<b>Grants Fetched</b>	<ul style="list-style-type: none"> <li>• Development of DSP and hardware lab (TAPTEC) sponsored by AICTE - completed II.</li> <li>• Coordinator of Up gradation of EDA Lab (MODROB) sponsored by AICTE - completed III.</li> <li>• Coordinator of ISTE of AICTE - sponsored two weeks winter School on Micro- controllers and their applications (1999).</li> </ul>
<b>16.</b>	<b>Interaction with professional institutions</b>	


1.	<b>Name:</b>	AMEETA SEEHRA 
2.	<b>Designation:</b>	Associate Professor
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	5th Aug. 1988
5.	<b>Qualifications:</b>	1.M.Tech.(Electronics and Comm.engg)[1997] 2. B.E. (Electronics and Comm.engg) [ 1986]
6.	<b>Experience</b>	1.AssociateProf.(G.N.D.E. College, Ludhiana)[1st Sept 1997 – till date.] 2. Lecturer(G.N.D.E. College, Ludhiana)[5th Aug. 1988 - 31st Aug. 1997.]
7.	<b>Paper published</b>	National Journals: Nil International Journals: 1
8.	<b>Paper presented in conferences</b>	Conferences:20
9.	<b>PhD guide- field and university</b>	
10.	<b>PhDs/ M.Tech thesis guided</b>	Master's:11 PhD:Nil
11.	<b>Books published/IPRs./ Patents</b>	Nil
12.	<b>Professional Memberships</b>	ISTE Member

<b>13.</b>	<b>Area of Consultancy</b>	
<b>14.</b>	<b>Awards</b>	
<b>15.</b>	<b>Grants Fetched</b>	Modernization and Expansion of existing Communication Lab(AICTE) – Rs. 8.5 Lacs
<b>16.</b>	<b>Interaction with professional institutions</b>	




1.	Name	<b>NARWANT SINGH GREWAL</b> 
2.	Designation:	Assistant Professor
3.	Department	Electronics and Communication Engineering
4.	Date of Joining	04-03-2002
5.	Qualifications:	Ph.D (Electronics & communication) Pursuing M.Tech(Electronics & Comm Engineering) [2002] B.E.(Electronics and comm.)
6.	Experience	1. Assistant professor/ Lecturer(G.N.D. Engg. College,Ludhiana)[ 05-03-2002 to till date] 2. Lecturer(Shanti Tara College,Ahmedgarh,Sangrur)[09-08-2001 to 31-12-2001] 3. Lectuer(L.L.R.M. Polytechnic, Ajitwal,Moga)[28- 03-99 to 08-08-2001] 4. Assistant Project Officer(Govt. Community Polytechnic, Ferozepure) [28-09-98 to 27-03-99] 5. Maintenance Engineer (Prabhat Spinning & Weaving Mills, Industrial Area, Ludhiana)[1-12-97 to 26-09-98] 6. Assistant Supervisor(Punjab Communication Ltd, c-135 PHASE -8 MOHALI ( 7-5-93 TO 6-10-94)
7.	Paper published	National journals : 03 International Journals : 04
8.	Paper presented in conferences	12


	<b>PH.D field and university</b>	
<b>10.</b>	<b>PhDs/ projects guided</b>	Master's: 06 PhD: Nil
<b>11.</b>	<b>Books published/IP Rs./Patents</b>	01
<b>12.</b>	<b>Professional Memberships</b>	ISTE,IE, IETE
<b>13.</b>	<b>Area of Consultancy</b>	Testing of isolation transformer at jindal electrical in 2006 Coordinated/participated in six week programme since 2006
<b>14.</b>	<b>Awards</b>	
<b>15.</b>	<b>Grants Fetched</b>	
<b>16.</b>	<b>Interaction with professional institutions</b>	

1.	<b>Name:</b>	<b>BALWINDER SINGH DHALIWAL</b>  
2.	<b>Designation:</b>	Assistant Professor
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	28 Aug. 2000
5.	<b>Qualifications</b>	Ph.D (Electronics & communication) Pursuing M.Tech(Electronics & communication)(2006) B.E.(Electronics & communication)[2000]
6.	<b>Experience</b>	Assistant Professor lecturer from 28 Aug. 2000 - till date
7.	<b>Paper published</b>	National Journals: 02 International Journals: 07
8.	<b>Paper presented in conferences</b>	Conferences: 55
9.	<b>PhD guide-field and university</b>	Dr. S.S. Pattnaik, Fractal Antenna optimization using soft computing, PTU Kapurthala
10.	<b>PhDs/ projects guided</b>	Master's: 25 PhD: Nil
11.	<b>Books published/IPR s./Patents</b>	❖ “ <i>DIGITAL ELECTRONICS</i> ” co-authored by Manpreet Singh Malhi and published by Kalyani Publishers, Ludhiana. ISBN No. 978-81-272-4759-1 (Year 2007). ❖ “Basic Electronics Engineering and Information Technology” co-authored by Jasdeep Kaur & Gaurav

		<p>Kumar and published by Kalyani Publishers, Ludhiana. ISBN No. 978-93-272-1886-2. (Year 2012)</p> <p>❖ “Deploying Sensor Nodes using Bacterial Foraging Optimization: Network Layout Methodology” co-authored by Gurjot Singh and Published by LAP LAMBERT Academic Publishing, Germany. ISBN No. 978-38-484-3261-5. (Year 2012)</p> <p>❖ “<i>DIGITAL ELECTRONICS</i>” co-authored by simranjit kaur Josan and published by Kalyani Publishers, Ludhiana. ISBN No 978-93-272-2880-9 (Year 2012).</p>
<b>12. Professional Memberships</b>	ISTE, IEEE	
<b>13. Area of Consultancy</b>	DSP, Antenna Design, Soft computing applications DST, PTU Grants for conduct of national conference	
<b>14. Awards</b>		
<b>15. Grants Fetched</b>	ISTE sponsorship for conducting STTP	
<b>16. Interaction with professional institutions</b>	As reviewer of journal, Expert in STTP	


1.	<b>Name:</b>	<b>Dr. MUNISH RATTAN</b>  
2.	<b>Designation:</b>	Assistant Professor
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	02-08-04
5.	<b>Qualifications:</b>	1.Ph.D. Engineering[2010] 2.M.E.( Electronics & communication)[2004] 3.B.Tech.( Electronics & communication)[2002]
6.	<b>Experience</b>	Assistant Professor/Lecturer From 02-08-04 to till date
7.	<b>Paper published</b>	National Journals: 08 International Journals: 12
8.	<b>Paper presented in conferences</b>	Conferences: 41
9.	<b>PhD guide- field and university</b>	Dr. Manjeet Singh pattar and Dr. B.S.Sohi ,Punjabi university
10.	<b>PhDs/ Thesis guided</b>	Master's: 22 PhD: 05( Guiding)
11.	<b>Books published/IPRs./Patents</b>	Nil
12.	<b>Professional Memberships</b>	IETE, ISTE
13.	<b>Area of Consultancy</b>	Expert faculty for six week training on VHDL, Coordinator for AMIE Lab Experiments
14.	<b>Awards</b>	Nil


15.	<b>Grants Fetched</b>	<p>1. Grant in aid for organizing REACT-11 from CSIR New Delhi, DST, New Delhi and PTU Kapurthala</p> <p>2. Grant in aid for organizing TEQUIP sponsored REACT-13 from DST, New Delhi and PTU Kapurthala</p>
16.	<b>Interaction with professional institutions</b>	


1.	<b>Name:</b>	<b>BALJEET KAUR</b>	
2.	<b>Designation:</b>	Assistant Professor	
3.	<b>Department</b>	Electronics and Communication Engineering	
4.	<b>Date of Joining</b>	July. 10	
5.	<b>Qualifications:</b>	1.Ph.D (Optical Communication) pursuing from NIT Hamirpur 2. M.E. (Electronics & communication)[2004] 3.B.E. (Electronics & communication ) [2001]	
6.	<b>Experience</b>	1.Assistant Prof.(G.N.D.Engg.College Ludhiana)[July. 10-till date] 2.Lecturer(LCET College Ludhiana)[02 March,05 to 07 July,06]	
7.	<b>Paper published</b>	National Journals: Nil International Journals: 02	
8.	<b>Paper presented in conferences</b>	Conferences: 14	
9.	<b>PhD guide-field and university</b>		
10.	<b>PhDs/ projects guided</b>	Master's: 01 PhD: Nil	
11.	<b>Books published/IPRs /Patents</b>	Nil	
12.	<b>Professional Memberships</b>	Nil	

<b>13.</b>	<b>Area of Consultancy</b>	Nil
<b>14.</b>	<b>Awards</b>	Nil
<b>15.</b>	<b>Grants Fetched</b>	Nil
<b>16.</b>	<b>Interaction with professional institutions</b>	Nil




1.	<b>Name:</b>	<b>HARMINDER KAUR</b> 
2.	<b>Designation:</b>	Assistant Professor
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	10 January 2011
5.	<b>Qualifications:</b>	M.Tech(Communication System)[2010] B.Tech(Electronics and Comm. )[2008]
6.	<b>Experience</b>	Assistant Professor(Gndec, Ludhiana)[10 January 2011- till now] Lecturer( RIEIT Engg. College ,Railmajra)[July 2010- 07 - January 2011]
7.	<b>Paper published in journals</b>	
8.	<b>Paper presented in conferences</b>	International conference; 01 national conference: 04
9.	<b>PhD guide- field and university</b>	N.A
10.	<b>PhDs/ projects guided</b>	Master's: 01 PhD: Nil
11.	<b>Books published/IPRs./Patents</b>	Nil
12.	<b>Professional Memberships</b>	ISTE
13.	<b>Area of Consultancy</b>	Faculty expert in six week training on VHDL
14.	<b>Awards</b>	Nil
15.	<b>Grants Fetched</b>	Nil
16.	<b>Interaction with professional institutions</b>	

1.	<b>Name:</b>	<b>Gurjot Kaur Walia</b> 
2.	<b>Designation:</b>	Assistant Professor
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	10 January 2011
5.	<b>Qualifications:</b>	M.Tech(Electronics and communication System)[2011] B.Tech(Electronics and Comm. )[2009]
6.	<b>Experience</b>	Assistant Professor(Gndec, Ludhiana)[10 January 2011- till now] Teaching Assistant (GNDEC,Ludhiana) [January, 2010 – December,2010 ]
7.	<b>Paper published</b>	
8.	<b>Paper presented in conferences</b>	National- 06 International - 02
9.	<b>PhD guide- field and university</b>	N.A
10.	<b>PhDs/ projects guided</b>	Master's: 01 PhD: Nil
11.	<b>Books published/IPRs./Patents</b>	nil
12.	<b>Professional Memberships</b>	NIL
13.	<b>Area of Consultancy</b>	Faculty expert in six week training on VHDL
14.	<b>Awards</b>	Nil
15.	<b>Grants Fetched</b>	Nil
16.	<b>Interaction with professional institutions</b>	

1	Name	Navneet Kaur 
2	Designation	Assistant Professor
3	Department	Electronics and Communication
4	Date of joining	December 20,2011
5	Qualification with class/Grade	1. Ph.D Pursuing 2.M.Tech(Electronics & communication) [2011] 3.B.Tech(Electronics & communication)
6	Total experience(year)	1.Assistant Professor (GNDEC,Ludhiana) [20 <sup>th</sup> Dec,2011- till date ]  2. Assistant Professor (LPU,Phagwara) [25 <sup>th</sup> July,2011-19 <sup>th</sup> Dec,2011]  3.Teaching Assistant (GNDEC,Ludhiana) [Aug'1 May '2011 ]
7	Papers published	International Journals:3 National Journal: 1
8	Papers presented in Conferences	National Conference:8 International Conference:2
9	Ph.D guide? Give field and university	Dr. Sandeep Singh Gill VLSI Design and Soft computing Techniques GNDEC Ludhiana PTU Kapurthala  Dr. Munish Rattan Antenna and optimization GNDEC Ludhiana PTU Kapurthala
10	Ph.D / projects guided	Nil
11	Books published/ IPRs/Patents	Nil

<b>12</b>	<b>Professional memberships</b>	Nil
<b>13</b>	<b>Area of Consultancy</b>	Faculty expert in six week training on VHDL
<b>14</b>	<b>Awards</b>	Gold Medalist in PTU, M.Tech(ECE)
<b>15</b>	<b>Grants fetched</b>	Nil
<b>16</b>	<b>Interaction with Professional Institutions</b>	

1.	<b>Name</b>	<b>Gurpurneet Kaur</b> 
2.	<b>Designation</b>	Assistant Professor
3.	<b>Department</b>	Electronics and Communication
4.	<b>Date of joining</b>	December 20,2011
5.	<b>Qualification with class/Grade</b>	1. Ph.D Pursuing 2.M.Tech(Electronics & communication) [2011] 2.B.Tech(Electronics & communication) [2008]
6.	<b>Total experience(year)</b>	1.Assistant Professor (GNDEC,Ludhiana) [20 <sup>th</sup> Dec,2011- till date ] 2. Assistant Professor (LPU,Phagwara) [25 <sup>th</sup> July,2011-19 <sup>th</sup> Dec,2011] 3.Teaching Assistant (GNDEC,Ludhiana) [Jan'10-May '2011 ] 4.Lecturer(BBSBPC,Fatehgarh Sahib) [17 <sup>th</sup> August,2008-18 <sup>th</sup> July,2009]
7.	<b>Papers published</b>	International Journals:3 National journal: 1
8.	<b>Papers presented in Conferences</b>	National Conference:7 International Conference:2
9.	<b>Ph.D guide? Give field and university</b>	Dr. Sandeep Singh Gill VLSI Design and Soft computing Techniques GNDEC Ludhiana, PTU Kapurthala  Dr. Munish Rattan Antenna and optimization GNDEC Ludhiana PTU Kapurthala

<b>10.</b>	<b>Ph.D / projects guided</b>	Nil
<b>11.</b>	<b>Books published/ IPRs/Patents</b>	Nil
<b>12.</b>	<b>Professional memberships</b>	Nil
<b>13.</b>	<b>Area of Consultancy</b>	Faculty expert in six week training on VHDL
<b>14.</b>	<b>Awards</b>	Nil
<b>15.</b>	<b>Grants fetched</b>	Nil
<b>16.</b>	<b>Interaction with professional Institutions</b>	

<b>1</b>	<b>Name</b>	<b>Chahat Jain</b> 
<b>2</b>	<b>Designation</b>	Assistant Professor
<b>3</b>	<b>Department</b>	Electronics and Communication
<b>4</b>	<b>Date of joining</b>	December 20,2011
<b>5</b>	<b>Qualification with class/Grade</b>	1. M.Tech(Electronics & communication) [2011] 2. B.Tech(Electronics & communication) [2009]
<b>6</b>	<b>Total experience(year)</b>	1.Assistant Professor (GNDEC,Ludhiana) [20 <sup>th</sup> Dec,2011- till date ]  2.Assistant Professor (NWIET,Moga) [8 <sup>th</sup> Aug,2011-19 <sup>th</sup> Dec,2011]  3.Teaching Assistant (GNDEC,Ludhiana) [Aug'10-Jan'2011 ]
<b>7</b>	<b>Papers published</b>	International Journals:4
<b>8</b>	<b>Papers presented in Conferences</b>	International Conference: 1 National Conference:6
<b>9</b>	<b>Ph.D guide? Give field and university</b>	Nil
<b>10</b>	<b>Ph.D / projects guided</b>	Nil
<b>11</b>	<b>Books published/ IPRs/Patents</b>	Nil
<b>12</b>	<b>Professional memberships</b>	Nil
<b>13</b>	<b>Consultancy activities</b>	Nil
<b>14</b>	<b>Awards</b>	Nil


<b>15</b>	<b>Grants fetched</b>	Nil
<b>16</b>	<b>Interaction with professional Institutions</b>	




1.	<b>Name:</b>	<b>Rohin Gupta</b> 
2.	<b>Designation:</b>	Assistant Professor
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	26 <sup>th</sup> December 2012
5.	<b>Qualifications:</b>	M Tech Pursuing B.Tech Electronics & communication
6.	<b>Experience</b>	2 years (Teaching)
7.	<b>Paper published</b>	nil
8.	<b>Paper presented in conferences</b>	National Conference: 02 International Conference: 02
9.	<b>PhD guide- field and university</b>	nil
10.	<b>PhDs/ projects guided</b>	nil
11.	<b>Books published/IPRs./Patents</b>	nil
12.	<b>Professional Memberships</b>	nil
13.	<b>Consultancy Activities</b>	Faculty expert in six week training on VHDL
14.	<b>Awards</b>	nil
15.	<b>Grants Fetched</b>	nil
16.	<b>Interaction with professional institutions</b>	Nil

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1.	Name:	<b>Kuldeepak Singh</b> 
2.	Designation:	Assistant Professor
3.	Department	Electronics and Communication Engineering
4.	Date of Joining	14 Aug.2013
5.	Qualifications:	M.Tech (Communication System) [2012]  B.Tech(Electronics and communication System) [2010]
6.	Experience	14 August 2013 to till date
7.	Paper published	International Journal: 03
8.	Paper presented in conferences	nil
9.	PhD guide- field and university	nil
10.	PhDs/ projects guided	nil
11.	Books published/IPRs./Patents	nil
12.	Professional Memberships	nil
13.	Area of Consultancy	nil
14.	Awards	nil

<b>15.</b>	<b>Grants Fetched</b>	nil
<b>16.</b>	<b>Interaction with professional institutions</b>	Nil

1.	<b>Name:</b>	<b>Harminder Kaur</b> 
2.	<b>Designation:</b>	Assistant Professor
3.	<b>Department</b>	Electronics and Communication Engineering
4.	<b>Date of Joining</b>	14 Aug 2013
5.	<b>Qualifications:</b>	M.Tech (Communication Systems) [2013]  B.Tech(Electronics and communication) [2010]
6.	<b>Experience</b>	14 August 2013 to till date
7.	<b>Paper published</b>	International Journal: 03
8.	<b>Paper presented in conferences</b>	International Conference: 01
9.	<b>PhD guide- field and university</b>	nil
10.	<b>PhDs/ projects guided</b>	nil
11.	<b>Books published/IPRs./Patents</b>	nil
12.	<b>Professional Memberships</b>	nil
13.	<b>Area of Consultancy</b>	nil
14.	<b>Achievements and Awards</b>	UGC NET Qualified for lectureship

<b>15.</b>	<b>Grants Fetched</b>	nil
<b>16.</b>	<b>Interaction with professional institutions</b>	Nil